

PATENT
4392-0153P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: HSIAO, Ching-Nan et al Conf.:
Appl. No.: NEW Group:
Filed: February 18, 2004 Examiner:
For: MULTI-BIT STACKED-TYPE NON-VOLATILE
MEMORY AND MANUFACTURE METHOD THEREOF

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 17, 2004

Sir:

The following preliminary amendments and remarks are respectfully submitted in connection with the above-identified application.

This amendment includes:

Amendments to the Specification

Remarks